module setting(clk,rst,sel,a,b,cin,result,cout1);

input clk,rst;

input [7:0]a,b;

input [2:0]sel;

input cin;

output cout1;

output [7:0]result;

wire c1,c2;

wire [7:0]re1,re2,re3;

wire en\_rca,en\_cla,en\_csa,en\_sh;

alu f(clk,rst,sel,en\_rca,en\_cla,en\_csa,en\_sh);

repple\_carry\_adder p(clk,rst,en\_rca,a,b,cin,c1,re1);

carry\_look\_ahead q(clk,rst,en\_cla,a,b,cin,c2,re2);

barrelshifter t(clk,rst,en\_sh,a,b,re3);

regi k(clk,rst,sel,c1,c2,re1,re2,re3,cout1,result);

endmodule